Docket No.: XA-9538

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Naoki YADA et al.

Application No.: Not Yet Assigned

Filed: Herewith

For: DATA PROCESSING SYSTEM AND

DATE PROCESSING METHOD

#2

McLean, Virginia August 27, 2001

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The documents on the attached List are cited in the specification, on pages 2-4, and their relevance is addressed therein.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees that may be required by this paper, and to credit any overpayment to that account.

Respectfully submitted, MILES & STOCKBRIDGE P.C.

Mitchell W. Shapiro

Registration No. 31,568

MILES & STOCKBRIDGE P.C. 1751 Pinnacle Drive, Suite 500 McLean, VA 22102-3833 703-903-9000

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)						TTY DOCKET NO. XA-9538		SERIAL NO.	010		
						Naoki YADA et al.			70. 23. 23.		
						FILING		GROUP	9.0° /	72/	
U.S. PATENT DOCUMENTS											
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			DOCUMENT NUMBER	DATE	COUNTRY CL/			SUBCLASS	SS TRANSLATION		
	ВА			10/29/1999	Japan				YES	NO	
		11-296392			<u> </u>				Abstrac	t	
	ВВ	3-057048		03/12/1991	Japan				Abstrac	t	
	вс	7-210215		08/11/1995	Japan				Abstrac	t	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)											
		Ikehashi et al., "A 60ns Access 32kByte 3-Transistor Flash for Low Power Embedded Applications," 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp 162-165.									
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Form PTO-A820 (also form PTO-1449)

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